

PATENT Attorney Docket No.: SAM-0486

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Yun-ho Choi, et al.

Examiner: Graybill, David E.

Serial No.:

10/714,801

Group Art Unit: 2822

Filing Date:

November 17, 2003

Title:

MULTI-CHIP PACKAGE HAVING INCREASED RELIABILITY

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

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Applicant(s): Yun-ho Choi, *et al.* Serial No.: 10/714,801

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[X]	Copies	s of cited references are enclosed.
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[]	The "c	concise explanation" requirement under 37 CFR 1.98(a)(3)(i) for cite no[s]. # is ed by:
	[]	the explanation provided on the attached sheet.
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	[]	citation of related [i.e. U.S. Patent no. x,xxx,xxx, U.S. Publication no. xxxx/xxxxxx, WO Patent no. xx/xxxxx].
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	[]	submission of abstract of corresponding Korean Publication No. xxxxxx.
	It is re	quested the information disclosed herein be made of record in this application.

Applicant(s): Yun-ho Choi, et al.

Serial No.: 10/714,801

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	Guidelines for Handling J-Lead, QFP & BGA Devices, Altera Corporation, January 1999, ver. 4, Application Note 71, pp. 1-16	
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